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PCB Design **Fabrication & Assembly** **Membrane Switches & Overlays**
Sheet Metal & Injection Molding **Laser Cut SMT Stencils**

PNC's Technology Roadmap

PNC's technology road map is a guide to enable us to schedule planned improvement in our current capabilities to serve our customer better. It also helps us to fulfill our commitment to AS9100D and ISO9001:2015 requirement of continuous improvement to achieve measurable customer satisfaction.

Following table shows our current capabilities as of JAN, 1st 2023 and future plans for 2024 and 2025.

No.	Manufacturing Capabilities	2023	2024	2025
1	Drilling Aspect Ratio	12:1	15:1	20:1
2	Minimum Drill Size	.006	.006	.004
3	Minimum Inner Layer Pad Size	.015	.014	.013
4	Minimum Outer Layer Pad Size	.012	.011	.010
5	Minimum Anti-Pad on Power/Ground Planes	.016	.014	.014
6	Minimum Buried/Blind Via Pad Size	.016	.014	.014
7	Minimum Buried/Blind Via Hole Size	.008	.006	.005
8	Minimum Inner Layer Line Width	.003	.003	.003
9	Minimum Outer Layer Line Width	.003	.0025	.002
10	Minimum Inner Layer Spacing	.003	.002	.0015
11	Minimum Outer Layer Spacing	.003	.0025	.002
12	Maximum Overall Board Thickness	.250	.250	.250
13	Minimum Dielectric Thickness	.003	.0025	.002
14	Minimum Component Pitch-Plating and Testing	.006	.006	.006
15	Layer to Layer Registration Tolerance	.003	.002	.0015
16	Conductor to Board Edge	.005	.004	.003
17	Solder Mask Clearance	.003	.003	.002
18	Solder Mask Dam between SMT Pads	.004	.003	.002
19	Minimum Base Copper Weight in oz.	.5	.25	.25
20	Maximum Overall Copper thickness	5	6	8
21	Maximum Layer Count	40	42	44
22	Plated Hole Tolerance	±.002	+.002/- .001	±.001
23	Maximum PCB Dimensions	19x25	19x25	19x25
24	Impedance Tolerance	±8%	±7.5%	±5%
26	Equipment Purchase Plan	Flying Prob – PCBA – 3Q, 2023 Pin-less Lamination 1Q, 2024 Automated MLB Press – Year 4Q, 2024 Laser Drill – 4Q, 2025		